B3CC: Concurrency 03:Threads (1)

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What is concurrency?

- Consider multiple tasks being executed by the computer...
 - Tasks are concurrent with respect to each other if:
 - They may be executed out-of-order
 - Implies they can be executed at the same time, but this is not required
 - Concurrency: deal with lots of things at once

What is parallelism?

- Consider multiple tasks being executed by the computer...
 - Tasks are parallel if they are executed simultaneously:
 - Requires multiple processing elements
 - program: parallel execution
 - Parallelism: do lots of things at once

• The primary motivation for parallel programming is to reduce the overall running time (wall clock) of the





- What does it mean for an application to be concurrent but not parallel?
 - Give an example
- What does it mean for an application to be parallel but not concurrent?
 - Give an example

Concurrency vs. Parallelism

- **Concurrency:** composition of independently executing processes
- Parallelism: simultaneous execution of (possibly related) computations





- Programming with multiple threads of control
 - A tool for structuring programs with multiple interactions
 - Examples: GUI, web server, different tasks in a game engine loop, ...
- There is no single right answer

• In this course we will discuss several approaches: it is up to you to pick which is right for your application

Not easy!



More concurrency

- Concurrency appears on many levels:
 - Threads within a process that share an address space (multithreading)
 - Processes on a single system (multiprogramming / multiprocessing)
 - Tasks on multiple systems connected by a network (distributed processing)

Hierarchy / "threads", "threads" and "threads"

CPU Specifications

Total Cores (?

Total Threads (?

CPU Specifications

Total Cores 🝞

of Performance-cores

of Efficient-cores

Total Threads 📀



24	24	
8	×	+
8	× 2	
16		



Hierarchy / "threads", "threads" and "threads"

- Physical CPU cores
- Logical CPU cores (simultaneous multithreading / (Intel) hyper-threading) ("threads")
- Kernel threads \rightarrow (scheduling: preemptive) \rightarrow "context switching"
- Processes
- User space threads / green threads / goroutines / ... (lightweight) \rightarrow (scheduling: either preemptive or

cooperative)



Processes & Threads

- A (kernel) *thread* is...
 - An execution context
 - Contains all the information a CPU needs to execute a (logically sequential) stream of instructions
- A process is...
 - A running instance of a computer program
 - Consists of at least one (kernel) thread
 - Separate memory space from other processes on the system
- Threads within a process share resources, but execute independently

• i.e. register set, stack, program counter (a.k.a. instruction pointer), (potentially) thread-local storage

- Many programming languages support threading in some capacity
 - Haskell: M:N hybrid threading model mapping M user space threads (forkIO) onto N kernel threads (via +RTS = -N < n >) \rightarrow user space threads
 - C/C++ provide access to the native threading APIs of the OS; POSIX threads (<u>pthread_create</u>) on *nix, and process.h (<u>beginthread</u>) on Windows.Various extensions can be built on top of these (OpenMP,TBB, ...)
 - Some interpreted languages (Ruby, Python) support threading for concurrency, but not parallel execution (GIL)
 - Some languages for parallel computing (CUDA, OpenCL) have "threads" in some sense, but in an entirely different way... more on that later!



Threads: needs and difficulties

- Concurrent processes (threads) need special support
 - Communication among processes
 - Allocation of processor time
 - Sharing of resources
 - Synchronisation of multiple processes
- Concurrency can be dangerous to the unwary programmer:
 - Sharing global resources (order of read & write operations)
 - Management of allocation of resources (danger of deadlock)
 - Programming errors are difficult to locate (Heisenbugs)



 \rightarrow race conditions!



Inserting:





- Inserting:
 - Create new object







- Inserting:
 - Create new object
 - Set last->next to &new





- Inserting:
 - Create new object
 - Set last->next to &new
 - Set last to &new





• Thread A:

• Thread B:



- Thread A:
 - Create new object



• Thread B:





- Thread A:
 - Create new object
 - Set last->next to &new

• Thread B:



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- Thread A:
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- Set last to &new

• Thread B:

- Create new object
- Set last->next to &new
- Set last to &new





- Lessons learned
 - We have to control access to shared resources (such as shared variables)
 - We can do this by controlling access to the code utilising those shared resources: critical sections



- Only one thread at a time should have access to the queue:
 - Thread A creates a new object, sets last->next pointer
 - Thread A is suspended
 - Thread B is scheduled: since Thread A is currently in insert, has to wait
 - Thread A is resumed, the data structure is in the same state as it was when it was suspended
 - Thread A completes operation
 - Thread B is allowed to execute insert



Concurrency control

- Processes/threads can
 - Compete for resources
 - Processes may not be aware of each other
 - Execution must not be affected by each other
 - OS is responsible for controlling access
 - Cooperate by sharing a common resource
 - Programmer responsible for controlling access
 - Hardware / OS / programming language may provide support
- Threads of a process usually do not compete, but cooperate



Concurrency control

- We face three control problems:
 - *Mutual exclusion*: critical resources => critical sections
 - Only one thread at a time is allowed in a critical section
 - e.g. only one thread at a time is allowed to send commands to the GPU
 - Deadlock: everyone is waiting on everyone else
 - Starvation: e.g. when one thread always gets left out :/



Mutual Exclusion



- Only one thread at a time should have access to the queue:
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 - Thread A is suspended
 - Thread B is scheduled: since Thread A is currently in insert, has to wait
 - Thread A is resumed, the data structure is in the same state as it was when it was suspended
 - Thread A completes operation
 - Thread B is allowed to execute insert



- Mutual exclusion (locking) protects shared resources
 - Only one thread at a time is allowed to access the critical resource
 - Modifications to the resource appear to happen atomically

mutex.lock();

... code ...

mutex.unlock();



Mutual exclusion

- Who is responsible?
 - Software approach: put responsibility on the processes themselves
 - Systems approach: provide support within the OS or programming language
- Hardware typically provides special-purpose machine instructions

• NOTE: Use the locking structures that come with your programming language! ... but let's try doing it ourselves anyway

Software approach to mutual exclusion

• Premise

- 2 threads with shared memory (no assumptions about relative thread speed)
- Elementary mutual exclusion at the level of memory access
 - Simultaneous accesses to the same memory location are serialised
- Requirements for the mutex:
 - Only one thread at a time is allowed in the critical section for a resource
 - No deadlock or starvation on attempting to enter/leave the critical section
 - A thread must not be delayed access to a critical section when there is no other thread using it
 - A thread that halts in its non-critical section must do so without interfering with other threads



Mutual exclusion

- Usage conditions:
 - A thread remains inside its critical section for a short time only
 - No potentially blocking operations should be executed inside the critical section



Attempt #I

- The plan:
 - Threads take turns executing the critical section
 - Exploit serialisation of memory access to implement serialisation of access to the critical section
- Thread A:

while (turn != 0) /* do nothing */ ;

<critical section>

turn = 1;

• Employ a shared variable (memory location) turn that indicates whose turn it is to enter the critical section

```
• Thread B:
  while (turn != 1)
    /* do nothing */;
  <critical section>
```

turn = 0;



Attempt #I

- Busy waiting (spin lock)
 - Process is always checking to see if it can enter the critical section
 - Implements mutual exclusion
 - Simple
- Disadvantages
 - Process burns resources while waiting
 - Processes must alternate access to the critical section
 - If one process fails anywhere in the program, the other is permanently blocked



Attempt #2

- The problem:
- The new plan:
 - Store for each process whether it is in the critical section right now
 - flag[i] if process i is in the critical section
- Thread A:

while (flag[1]) /* do nothing */ ; flag[0] = true; <critical section> flag[0] = false;



- turn stores who can enter the critical section, rather than whether anybody may enter the critical section

• Thread B: while (flag[0]) /* do nothing */ ; flag[1] = true; <critical section> flag[1] = false;





- Does not guarantee exclusive access
- Race condition: time-of-check to time-of-use (TOCTOU)
- What if a process fails?
 - Outside the critical section: the other is not blocked *V*
 - Inside the critical section: the other is blocked :/ (however, difficult to avoid)



Attempt #3

- The goal:
 - Remove the gap between toggling the two flags
- The new updated plan:
 - Move setting the flag to before checking whether we can enter
- Thread A:

flag[0] = true;

while (flag[1]) /* do nothing */ ;



<critical section> flag[0] = false;

• Thread B:

flag[1] = true;

while (flag[0]) /* do nothing */ ;

<critical section> flag[1] = false;





- Is it working now?
 - No.The gap can cause a deadlock now >_>
 - Deadlock: when each member of a group of threads is release a lock)

- Deadlock: when each member of a group of threads is waiting for another to take action (e.g. waiting for another to



Attempt #4

- Previous problem:
 - Thread sets its own state before knowing the other threads' states, and cannot back off
- The new updated revised plan:
 - Thread retracts its decision if it cannot enter

```
• Thread A:
```

```
flag[0] = true;
while (flag[1]) {
  flag[0] = false;
  delay();
  flag[0] = true;
<critical section>
flag[0] = false;
```

```
• Thread B:
  flag[1] = true;
  while (flag[0]) {
    flag[1] = false;
    delay();
```

```
flag[1] = true;
<critical section>
flag[1] = false;
```





- Is it working now?
 - Close, but we may have a livelock = _=
 - (e.g. trying to obtaining a lock, but backing off if it fails)
 - deadlock

- Livelock: The states of the group of threads are constantly changing with regard to each other, but none are progressing

- A special case of resource starvation, and a risk for algorithms which attempt to detect and recover from





- Improvements
 - We can solve this problem by combining the first and third attempts
 - critical section

- In addition to the flags we use a variable indicating whose turn it is to have precedence in entering the

Attempt #5: Peterson's algorithm

- Both threads are courteous and solve a tie in favour of the other
- Algorithm can be generalised to work with n threads
- Thread A:

```
flag[0] = true;
turn = 1;
while (flag[1]
   && turn == 1)
  /* do nothing */;
```

<critical section>

flag[0] = false;

https://en.wikipedia.org/wiki/Peterson%27s_algorithm

• Thread B: flag[1] = true; turn = 0;while (flag[0] && turn == 0) /* do nothing */ ; <critical section> flag[1] = false;



Attempt #5: Peterson's algorithm

- Statement: mutual exclusion Threads 0 and 1 are never in the critical section at the same time
- Proof:
 - If P₀ is in the critical section then
 - flag[0] is true
 - true but before setting turn to zero
 - For both P₀ and P₁ to be in the critical section
 - flag[0] AND flag[1] AND turn=0 AND turn=1



flag[1] is false OR turn is zero OR P₁ is trying to enter the critical section, after setting flag[1] to

Locking: real life

- Again: Peterson's algorithm is a theoretical exercise
- Please use the facilities in your programming language
- operation (casIORef)! (explained on Monday)

• If you are implementing a mutex yourself (or are doing the first practical, IBAN!), use the compare-and-swap



For the practical



IORefs

- In most languages variables are mutable by default
- In Haskell, mutable variables must be handled explicitly
 - Notice that whether a variable is mutable is now reflected in its type!

import Data.IORef

newIORef	•••	a ->	IO
readIORef	•••	IORef	a
writeIORef	• •	IORef	a

- More information on Monday
- Check the documentation!
 - <u>https://hackage.haskell.org/package/base-4.17.2.1/docs/Data-IORef.html</u>
 - https://hackage.haskell.org/package/atomic-primops-0.8.8/docs/Data-Atomics.html

```
(IORef a)
-> IO a
-> a -> IO ()
```



Extra slides

Not all CPU operations are created equal



Operation Cost in CPU Cycles	10 °	10 ¹	10 ²	10 ³	10 ⁴	10 ⁵	10 ⁶
gister-register op (ADD,OR,etc.)	<1						
Memory write	~1						
Bypass delay: switch between							
integer and floating-point units	0-3						
"Right" branch of "if"	1-2						
Floating-point/vector addition	1-3						
ultiplication (integer/float/vector)	1-7						
Return error and check	1-7						
L1 read		3-4					
TLB miss		7-21					
L2 read		10-12					
nch of "if" (branch misprediction)		10-20					
Floating-point division		10-40					
128-bit vector division		10-70					
Atomics/CAS		15-30					
C function direct call		15-30					
Integer division		15-40					
C function indirect call		20-50					
C++ virtual function call		30	-60				
L3 read		30	-70				
Main RAM read			100-150				
1A: different-socket atomics/CAS			100-300				
(guesstimate)							
NUMA: different-socket L3 read			100-300				
deallocation pair (small objects)			200-5	00			
different-socket main RAM read			300	0-500			
Kernel call				1000-150	0		
ead context switch (direct costs)				2000			
C++ Exception thrown+caught				500	00-10000		
nread context switch (total costs,					<u> 10000 - 1</u>	million	
including cache invalidation)							

ithare.com Operation Cost in CPU Cycles	10 º	10 ¹	10 ²	10 ³	10⁴	10 ⁵	10 ⁶
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Bypass delay: switch between							
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Multiplication (integer/float/vector)	1-7						
Return error and check	1-7						
L1 read	3-4						
TLB miss		7-21					
L2 read		10-12					
"Wrong" branch of "if" (branch misprediction)		10-20					
Floating-point division		10-40					
128-bit vector division		10-70					
Atomics/CAS		15-30					
C function direct call		15-30					
Integer division		15-40					
C function indirect call		20-50					
C++ virtual function call		30-	60				
L3 read		30-	70				
Main RAM read			100-150				
NUMA: different-socket atomics/CAS			100 200				
(guesstimate)							
NUMA: different-socket L3 read			100-300				
Allocation+deallocation pair (small objects)			200-50	0			
NUMA: different-socket main RAM read			300-	500			
Kernel call				1000-150	0		
Thread context switch (direct costs)				2000			
C++ Exception thrown+caught				50	00-10000		
Thread context switch (total costs,					10000-1	million	
including cache invalidation)							

Distance which light travels while the operation is performed





